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| Application Number:                  | 10595303  |  |
| International Application Number:    |   |  |
| Confirmation Number:                 | 7114  |  |
| Title of Invention:                  | Method and Device For Secure, Insulated and Electrically Conductive<br>Assembling Of Treated Semiconductor Wafers |  |
| First Named Inventor/Applicant Name: | Roy Knechtel  |  |
| Customer Number:                     | 21967   |  |
| Filer:                               | Yisun Song/Robin Taylor   |  |
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**Document Description** 

Information Disclosure Statement (IDS) Filed (SB/08)

**NPL Documents** 

Multipart Description/PDF files in .zip description

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## New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

## National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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